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	Filing Date		2005-04-27	
	First Named Inventor	Koji Kikushima		
	Art Unit	4177		
	Examiner Name	Dexter Hodge		
	Attorney Docket Number	6700-85315		

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	3	97/19504	WO	A1	1997-05-29	The Secretary of State for Defence (GB)		<input type="checkbox"/>

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4	09-326769	JP	A	1997-12-16	Nippon Telegraph and Telephone Corporation	<input checked="" type="checkbox"/>
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	1	OKA, TATSUTO et al.; A 1-5GHz Low-Power Single-Chip Receiver IC for Optical Video Distribution System, 2001 IEEE Radio Frequency Integrated circuits (RFIC) Symposium, Digest of Papers, Phoenix, AZ, May 20-22, 2001, IEEE Radio Frequency Integrated Circuits Symposium, New York, NY: IEEE, US, 20 May 2001, pages 199-202.	<input type="checkbox"/>

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